

Docket No.: 50006-138

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Atsushi SAKAI, et al.	:	Confirmation Number: 9551
Application No.: 10/076,625	:	Group Art Unit: 2188
Filed: February 19, 2002	:	Examiner: John M. Ross

For: CACHE MEMORY SYSTEM HAVING HARDWARE AND SOFTWARE CACHE CONTROLLERS (AS AMENDED)

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with a Request for Continued Examination before the mailing date of a first Office Action on the merits. No certification or fee is required.

Each non-English language reference contains an English-language abstract and was first cited in a corresponding foreign application search report or office action and its relevance

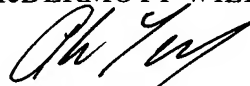
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discussed therein. A copy of the foreign search report or office action, is attached for the Examiner's information.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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**Please recognize our Customer No. 20277  
as our correspondence address.**

INFORMATION DISCLOSURE CITATION IN AN APPLICATION  (PTO-1449)		ATTY. DOCKET NO. <b>50006-138</b>		SERIAL NO. <b>10/076,625</b>		
		APPLICANT <b>Atsushi SAKAI, et al.</b>				
		FILING DATE <b>February 19, 2002</b>		GROUP <b>2188</b>		
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sub>2</sub> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number 4 -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
		SAKAMOTO, Katsuto., et al. "Software Controlled Cache for Multi-grain Parallel Processing." The Institute of Electronics, Information and Communications Engineers, Technical Report of IEICE, ICD98-26, CPSY98-11-26, April 4, 24, 1998, pp. 117-124				
		IWAI, K., et al. "ASCA: A multiprocessor architecture Initiated by a compiler." Joint Symposium on Parallel Processing 2000, JSPP2000, May 12, 2000, pp. 3-10				
		NAKAMURA, Hiroshi., et al. "SCIMA: A New Architecture for High Performance Computing." Proceedings of Information Processing Society of Japan: Transactions on High Performance Computing Systems." August 15, 2000, Volume 41. Number: SIG5(HPS1) pp. 15-27				
EXAMINER			DATE CONSIDERED			